

# *Chops Acts Update*

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# Video Meeting Summary

## April 9, 2024

- **Subject :NSTC**
- **Participants**
- **Deirdre Hanford, CEO of Natcast and**
- **Jay Lewis, NSTC Program Director**
- **Priorities**
  1. **Workforce development (on location)**
  2. **Innovation (Technology and Design)**
    1. **Venture Funds**
  3. **Research**



# CHIPS R&D Programs



CHIPS for America | Revised 1

## Chips Act Investment Status

							40.00%	
Company	Plants	Investments	\$	Loans	Tax credit	Employees	Techs	Construction
Intel	3	100	8.5	11	25%	10,000	4,000	20,000
TSMC	3	65	6.6	5	25%	6,000	2,400	20,000
Samsung	2	40	6.5		25%	4,500	1,800	17,000
Tot	8	205	21.6	16		20,500	8,200	57,000

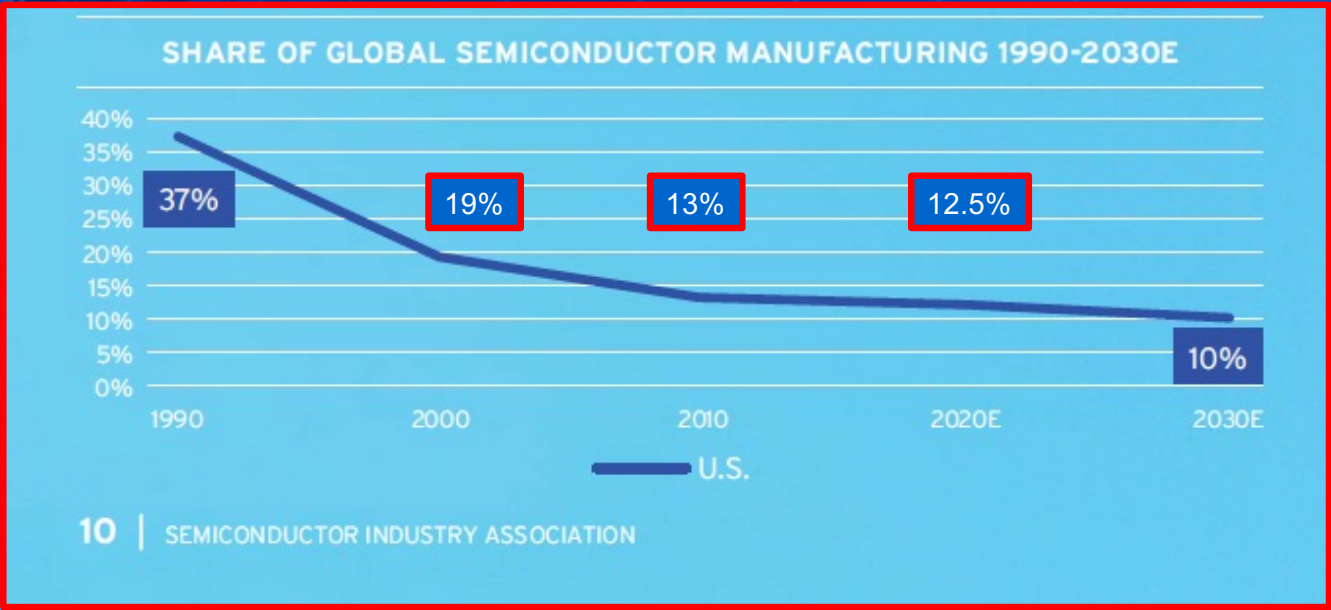
Workforce	Employees	Techs	Construction
	20,500	8,200	57,000

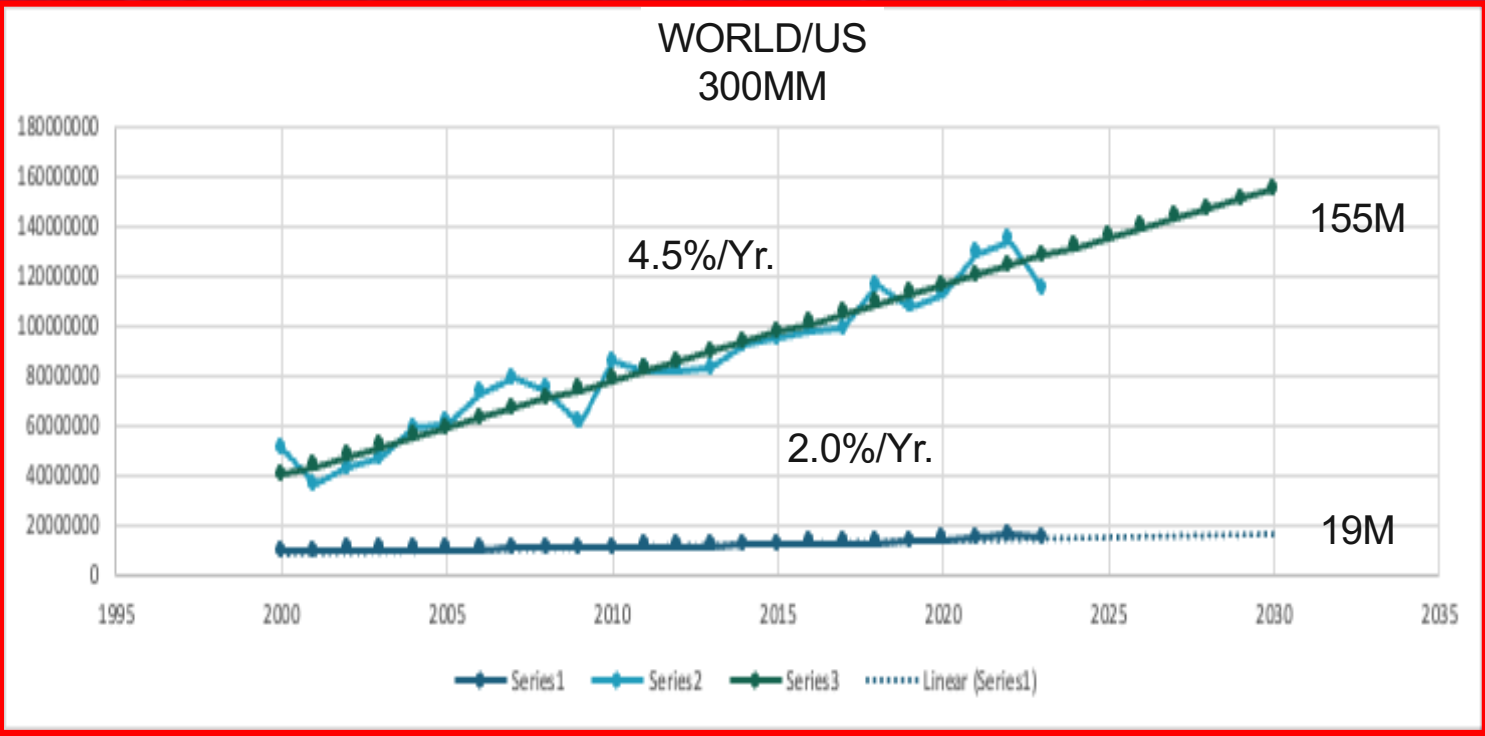


TSMC	2,019	2,020	2,021	2,022	2,023
EMPLOYEES	51,297	56,831	65,152	73,090	76,478
WAFERS (M)	10	12	14	15	12
CAPACITY	12	13	14	16	17
EMPL/M WAFERS	5,130	4,736	4,654	4,873	6,373
TECHNICIANS	1,716	1,531	1,427	2,129	1,881
RATIO	33%	32%	31%	44%	30%

### Workforce Structure

		12/31/2022	12/31/2023	02/29/2024
Job	Managers	7,295	7,861	8,000
	Professionals	35,189	36,807	37,129
	Assistant Engineer/Clerical	8,665	9,235	9,284
	Technicians	21,941	22,575	22,620
Total		73,090	76,478	77,033
Gender	Male	65.6%	65.8%	65.9%
	Female	34.4%	34.2%	34.1%
Education	Ph.D.	3.8%	3.9%	3.9%
	Master's	47.2%	47.7%	47.9%
	Bachelor's	29.3%	29.5%	29.5%
	Other Higher Education	8.4%	8.0%	8.0%
	High School	11.3%	10.9%	10.9%
Average Age		35.7	36.2	36.2
Average Years of Service		8.3	8.7	8.8







300mm	2021	2022	2023	2024-2030	2030
<b>MWPY</b>	15	16	15		19
<b>Intel</b>				1.5	
<b>Micron</b>				1.0	
<b>GF</b>				0.5	
<b>Tot adds</b>				3	
<b>TSMC</b>				1.5	
<b>Samsung</b>				1.0	
<b>Tot T+S</b>				2.5	2.5
<b>Grand Tot</b>					21.5
<b>World</b>	129	134	115		155
<b>Ratio</b>	12%	12%	13%		14%

<b>TSMC</b>	14	16	17
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